

A Broken Line Detection Circuit for Multi-cell Li-ion Battery Module¹

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Abstract—This paper presents a novel broken line detection circuit for multi-cell Li-ion Battery modules. The broken line detection technique detects test lines between the battery pack and any following circuit, e.g., BMS (battery management system). The core of the broken line detection circuit consists of an RC filter, a Brokenline Detection Block, and a bias voltage source. A system-level experiment verifies that the proposed circuit can reliably detect any disconnection of Li-ion battery pack and the following circuit to ensure the safety of the system.

I. INTRODUCTION

Recently, many Li-ion battery packs are used in high-voltage and high-spindle equipments or systems such as electric vehicles (EV) [1], UPS, and high-power electric tools [2]. The existing Li-ion battery is usually equipped with overcharge, and over-discharge detection ICs to ensure the safety of the battery cell when multiple cells are connected in series. However, the existing protection IC cannot check the disconnection, namely open circuit, for each battery cell. The undetected open circuit in a battery pack might result in wrong readings of the battery state, and even safety issues. Therefore, to constantly and accurately detect perfect connection of the battery detection lines, a battery disconnection detection circuit is proposed to resolved the mentioned problems [3][4][5]. Fig. 1 shows a typical battery-powered system in an EV, where Broken Line Detector is positioned between the Battery Module and BMS. It is composed of an RC filter, a bias voltage source, a current source, PMOS and NMOS switches. As soon as a detection line is opened, the corresponding MOS switch will be turned on to pull up a warning flag signal ($V_{outi(i=1,2,3)}$) from low to high, to the following circuit such that the BMS can take corresponding actions.

II. BROKEN LINE DETECTOR (BLD) DESIGN

Fig. 2 shows the operation flow of the Broken Line Detector (BLD). The illustrative example is equipped with 4 Li-ion battery cells, the RC filter and Brokenline Detection Block, as is shown in Fig. 3. To detect open or short of each lines, every battery is coupled to a corresponding MOS switch.

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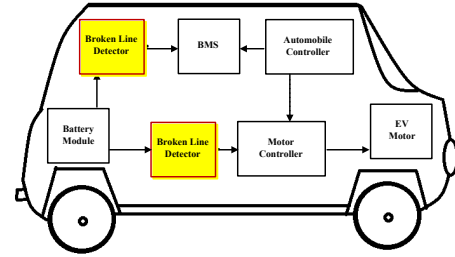


Fig. 1. A typical power system in an EV with Broken Line Detectors.

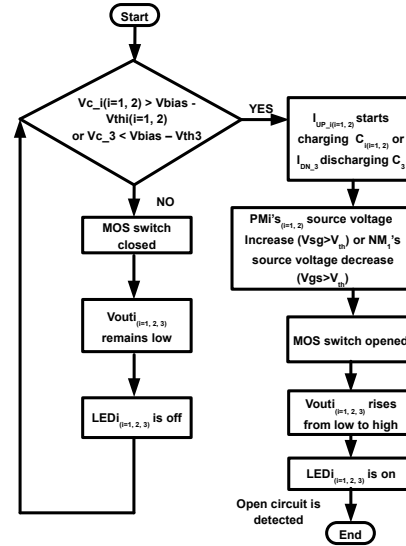


Fig. 2. The operation flow of Broken Line Detector.

To generate V_{bias} , V_{cc} is divided by two series resistors, i.e., R_4 and $1.66R_4$. The reason of the proportion between the two series resistors is set the V_{bias} voltage between those of $CELL_2$ and $CELL_3$ positive electrodes.

PM_1 and PM_2 are PMOS transistors, where the gate is driven by V_{bias} . Their sources are coupled to the positive electrodes of $CELL_1$ and $CELL_2$, respectively. I_{UP_1} and I_{UP_2} are pull-up currents which are designed much larger than I_{B_1} and I_{B_2} , namely the corresponding current sinks.

If L_1 and L_2 are good, PM_1 and PM_2 are closed. V_{out1} and V_{out2} stay low. If L_1 or L_2 is open, I_{UP_1} or I_{UP_2} starts to charge the capacitor C_1 or C_2 , and raise $V_{c_i}(i=1,2)$ to turn on PM_1 or PM_2 , respectively. V_{out1} or V_{out2} will then be pulled up from low to high to indicate that a broken line is detected.

The open circuit detection for the line of the top battery cell is different. NM_1 is an NMOS, where the gate voltage is driven by V_{bias} , and the source is connected to the positive electrode of $CELL_3$. I_{DN_3} is a current sink much larger than I_{B_3} . If L_3 is good, NM_1 is closed and V_{out3} remains low. If L_3 is open, the capacitor C_3 starts to discharge by I_{DN_3} and pull-down V_{c_3} to turn on NM_1 . The drain voltage of NM_1 is then pulled low, and the V_{out3} will turn high by the inverter.

In short, any line coupled to the cells of a battery string can be independently detected to learn if the connection is good.

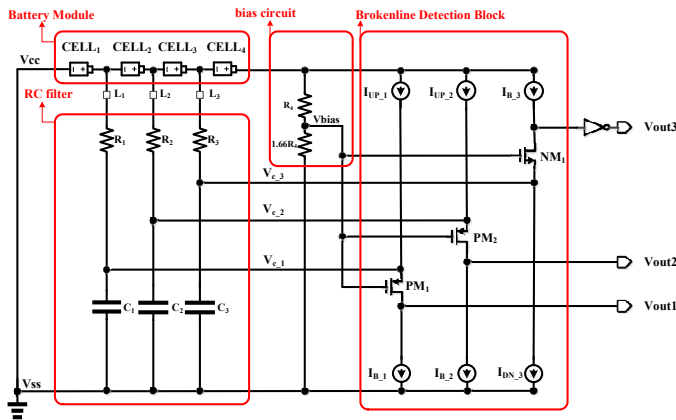


Fig. 3. Block diagram of a four cells Li-ion battery protection circuit.

III. EXPERIMENT AND MEASUREMENT

A system prototype setup for the proposed Broken Line Circuit and battery module (ICR18650-26H) measurement is shown in Fig. 4. The results are shown in Fig. 5. In Fig. 5 (a) shows the scenario when all lines are good. Fig. 5 (b) shows L_1 is open, Fig. 5 (c) shows L_1 and L_3 are open, and Fig. 5 (d) shows L_1 , L_2 , and L_3 are all open. The $LED_{i(i=1,2,3)}$ on-off states in Fig. 5 indicate whether the proposed Broken Line Detector successfully detect the open circuits of all the lines. A comparison between the proposed Broken Line Detector and several prior works is tabulated in Table I, where our design demonstrates the full functionality of open line deflection for the widest battery voltage range.

TABLE I
PERFORMANCE COMPARISON OF BROKEN LINE DETECTOR

| | [3] | [4] | [5] | this work |
|-------------------------------|------|----------|----------|-----------|
| Year | 2011 | 2013 | 2011 | 2018 |
| Multi-cell detection | N/A | Yes | Yes | Yes |
| Voltage broken line detection | Yes | Yes | Yes | Yes |
| Voltage range (V) | N/A | 0 – 14.4 | 0 – 14.4 | 0 – 16.8 |

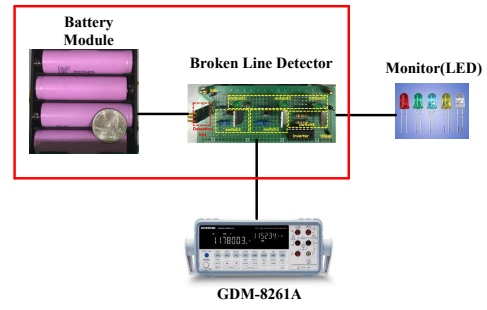


Fig. 4. Measurement setup of the proposed Broken Line circuit.

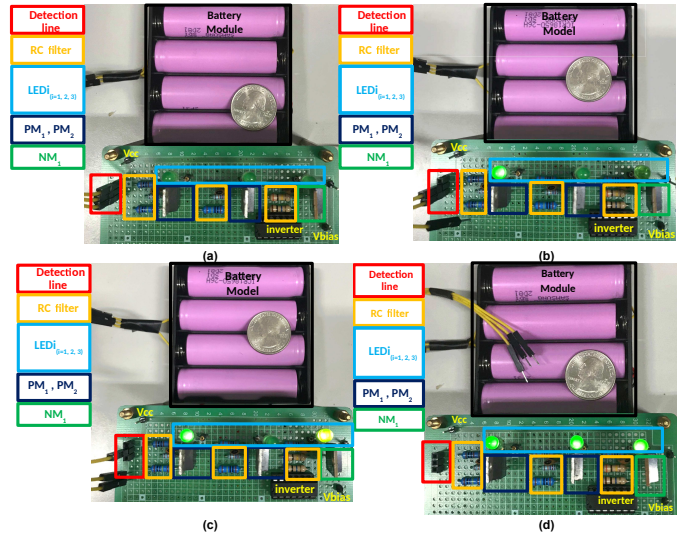


Fig. 5. The measurement results with (a) none, (b) 1, (c) 2, (d) 3 battery cells disconnected.

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